

A

B

C

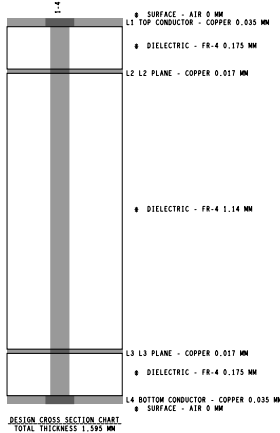
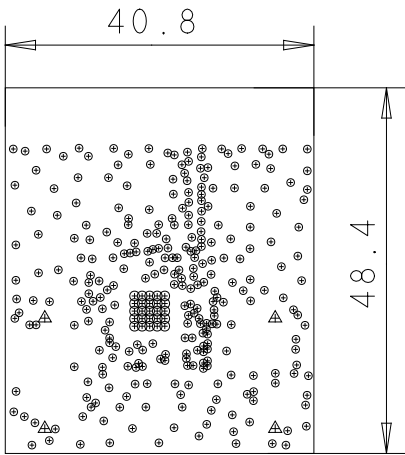
D

A

B

C

D

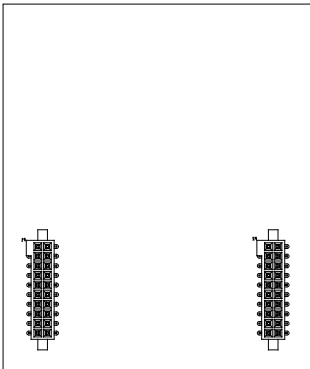


DRILL CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILLIMETERS			
FIGURE	FINISHED_SIZE	PLATED	QTY
⊙	0.2	PLATED	280
⊙	0.2	PLATED	25
△	1.501	NON-PLATED	4

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DESIGN INFORMATION		
MIN. TRACK WIDTH:	0.15 mm	
MIN. CLEARANCE:	0.15 mm	
MIN. VIA PAD SIZE:	mm	
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C REGISTRATION TOLERANCES: METAL +/-150 um, HOLES +/-80 um HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/-80 um		
MATERIAL:		
<input type="checkbox"/> FR-408	<input type="checkbox"/> FR-4 High Tg	<input type="checkbox"/> OTHER
THICKNESS: <input checked="" type="checkbox"/> 1.6mm +/-10%	<input type="checkbox"/> OTHER	
TOLERANCE: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2	<input type="checkbox"/> OTHER +/-	
BOW & TWIST: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2	<input type="checkbox"/> OTHER +/-	
DRILLING:		
REFERENCE: <input checked="" type="checkbox"/> AS SHOWN	<input checked="" type="checkbox"/> NC_DRILL FILES	
PTH COPPER THICKNESS: <input checked="" type="checkbox"/> 20-30 um	<input type="checkbox"/> OTHER	
BOARD FINISH:		
SILKSCREEN: <input checked="" type="checkbox"/> TOP	<input checked="" type="checkbox"/> BOTTOM	
SILKSCREEN COLOR: <input checked="" type="checkbox"/> WHITE	<input type="checkbox"/> OTHER	
SILKSCREEN RESIST COLOR: <input checked="" type="checkbox"/> GREEN	<input type="checkbox"/> OTHER	
<input checked="" type="checkbox"/> MATTE	<input type="checkbox"/> SEMI-GLOSS	
SURFACE FINISH: <input checked="" type="checkbox"/> IMMERSION GOLD (ENIG)	<input type="checkbox"/> ENIG	
<input type="checkbox"/> IMM. TIN/SILVER OR EQUIV	<input type="checkbox"/> OTHER	
ARRAY/PANEL: <input type="checkbox"/> CUT AND TRIM PER M1 BOARD OUTLINE	<input checked="" type="checkbox"/> N.C. ROUTE	
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:	<input checked="" type="checkbox"/> V. SCORE	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS	->	<input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3
<input checked="" type="checkbox"/> RoHS	<input type="checkbox"/> OTHER PER ORDER	
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.		
PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER		
ADDITIONAL REQUIREMENTS:		
MICROSECTION: <input type="checkbox"/> YES	<input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER	
BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE	<input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER	
<input type="checkbox"/> XX MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE	<input type="checkbox"/> XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> OUTER XX MIL VIAS REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE	<input type="checkbox"/> LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE	
<input type="checkbox"/> TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE		
TITLE: CC1354P IPCs		
PROJECT NUMBER: CC1354P IPCs		
FILE NAME: filename.brd		
DESIGNER: RGW	DATE: 2023-09-06	REVISION: E1
SCALE: 1.00		ALLEGRO DESIGNER VERSION: 17.4

ART FILM - assembly_bottom



ART FILM - assembly_bottom